

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JEFFREY SCOTT ET AL.

Filed: FEBRUARY 22, 2002

For: CALIBRATED LOW-NOISE CURRENT AND VOLTAGE
REFERENCES AND ASSOCIATED METHODS

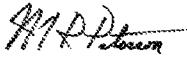
Serial No.: 10/081,121

Group Art Unit: 2682

Examiner: TRAN, T.

Att'y Docket: SILA:095

I hereby certify that this correspondence is being transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Date: December 11, 2006 Name: 

Commissioner for Patents
P.O. Box 1450
Alexandria 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

The Applicant acknowledges receiving a "Notice Regarding Drawings" dated October 20, 2006, stating that the previously submitted drawings are not acceptable to the Office. In response, the Applicant encloses with this paper a set of formal drawings, with each sheet marked "Replacement Sheet." Please enter the drawings in the application.

The Applicant believes that no fees are due in connection with this paper. Should any fees under 37 C.F.R. §§ 1.16-1.21 be required for any reason relating to the enclosed materials, however, the Commissioner is authorized to deduct such fees from Deposit Account No. 50-3813/SILA:095.

The Applicant invites the Examiner is invited to contact the undersigned at the telephone number indicated below with any questions or comments, or to otherwise facilitate expeditious and compact prosecution of the application.

Respectfully submitted,



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